

FORM PTO-1449		Docket Number (Optional) 100-19410 (P05247-D01)	Application No. New (Div. of 10/272,035)
<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b> <i>(Use several sheets if necessary)</i>  Express Mail Number <u>EV364033438US</u>		Applicant(s) James Thomas Doyle et al.	
		Filing Date Herewith	Group Art Unit Unknown
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>			
UKT	*Dragan Maksimovic, Bruno Kranzen, Sandeep Dhar and Ravindra Ambatipudi, U.S. Patent Application No. 10/053,226, filed January 19, 2002, entitled "An Adaptive Voltage Scaling Digital Processing Component and Method of Operating the Same".		
UKT	*Bruno Kranzen and Dragan Maksimovic, U.S. Patent Application No. 10/053,227, filed January 19, 2002, entitled "Adaptive Voltage Scaling Clock Generator for Use in a Digital Processing Component and Method of Operating the Same".		
UKT	*Dragan Maksimovic and Sandeep Dhar, U.S. Patent Application No. <del>10/053,828</del> , filed January 19, 2002, entitled "System for Adjusting a Power Supply Level of a Digital Processing Component and Method of Operating the Same". <u>10/053,858</u> UKT		
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UKT	*James T. Doyle and Dragan Maksimovic, U.S. Patent Application No. <del>10/166,428</del> , filed March 26, 2002, entitled "Method and System for Minimizing Power Consumption in Mobile Devices Using Cooperative Adaptive Voltage and Threshold Scaling".		
UKT	*Dragan Maksimovic and James Thomas Doyle, U.S. Patent Application No. 10/166,822, filed June 10, 2002, entitled "Serial Digital Communication Superimposed on a Digital Signal Over a Single Wire".		
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UKT	*Robert W. Erickson and Dragan Maksimovic, <u>Fundamentals of Power Electronics</u> , Second Edition, Kluwer Academic Publishers, 2001, pp. 333.		
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EXAMINER	LONG TRAN	DATE CONSIDERED	1/12/05
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